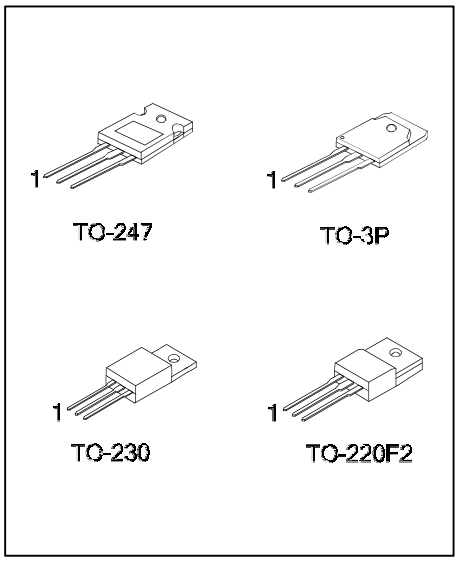
NJ12N80 POWER MOSFET

12A, 800V N-CHANNEL POWER MOSFET



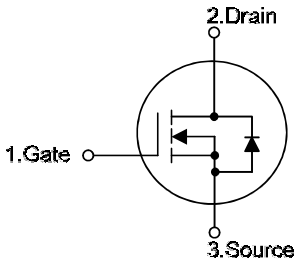
■ DESCRIPTION

The NJ12N80 uses advanced proprietary, planar stripe, DMOS technology to provide excellent RDS(ON), low gate charge and operation with low gate voltages. This device is suitable for use as a load switch or in PWM applications.

■ FEATURES

* $R_{DS(on)}$ < 1.0 Ω @ V_{GS}=10V
* High switching speed
* Improved dv/dt capability
* 100% avalanche tested

■ SYMBOL



■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
NJ12N80L-T47-T	NJ12N80G-T47-T	TO-247	G	D	S	Tube
NJ12N80L-T3P-T	NJ12N80G-T3P-T	TO-3P	G	D	S	Tube
NJ12N80L-TC3-T	NJ12N80G-TC3-T	TO-230	G	D	S	Tube
NJ12N80L-TF2-T	NJ12N80G-TF2-T	TO-220F2	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source

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■ ABSOLUTE MAXIMUM RATINGS (T_C = 25°C unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	800	V
Gate-Source Voltage		V_{GSS}	±30	V
Drain Current	Continuous (T_C=25°C)	I_D	12	A
	Pulsed (Note 2)	I_{DM}	48	A
Avalanche Current (Note 2)		I_{AR}	12	A
Power Dissipation	TO-247	P_D	360	W
	TO-3P		390	W
	TO-230		167	W
	TO-220F2		51	W
Junction Temperature		T_J	+150	°C
Storage Temperature		T_{STG}	-55~+150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2. Repetitive Rating: Pulse width limited by maximum junction temperature.

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-247	θ_{JA}	50	°C/W
	TO-3P		40	°C/W
	TO-220F2/TO-230		62.5	°C/W
Junction to Case	TO-247	θ_{JC}	0.35	°C/W
	TO-3P		0.32	°C/W
	TO-230		0.75	°C/W
	TO-220F2		2.43	°C/W

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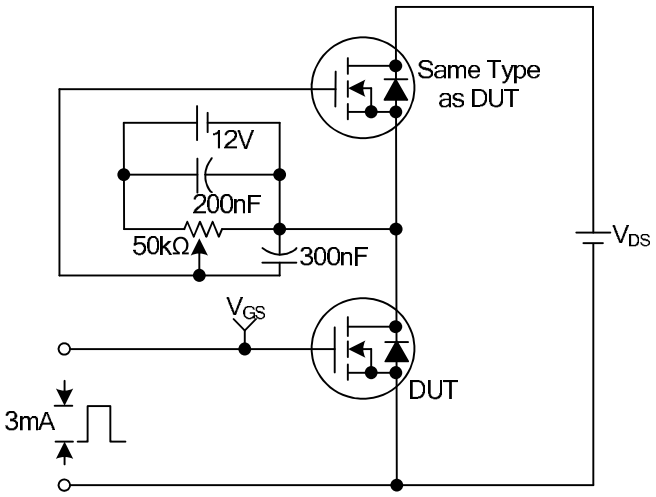
■ ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise specified)

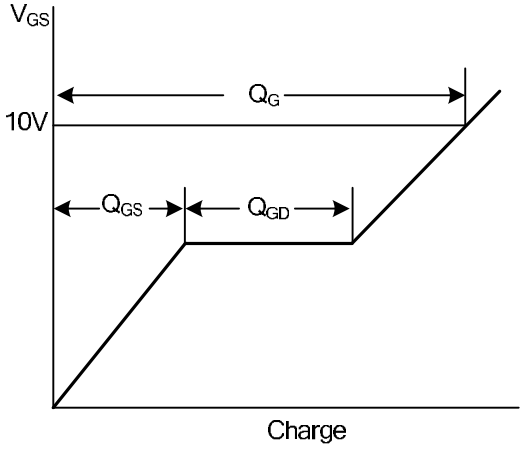
PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV_{DSS}	I_D=250μA, V_{GS}=0V	800			V
Breakdown Voltage Temperature Coefficient		$\triangle BV_{DSS}/\triangle T_J$	I_D=250μA, Referenced to 25°C		1.0		V/°C
Drain-Source Leakage Current		I_{DSS}	V_{DS}=800V, V_{GS}=0V			10	μA
			V_{DS}=640V, T_C=125°C			100	
Gate- Source Leakage Current	Forward	I_{GSS}	V_{GS}=+30V, V_{DS}=0V			100	nA
	Reverse		V_{GS}=-30V, V_{DS}=0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	V_{DS}=V_{GS}, I_D=250μA	3.0		5.0	V
Static Drain-Source On-State Resistance		$R_{DS(ON)}$	V_{GS}=10V, I_D=6A		0.75	1.0	Ω
DYNAMIC PARAMETERS							
Input Capacitance		C_{ISS}	V_{GS}=0V, V_{DS}=25V, f=1.0MHz		4200		pF
Output Capacitance		C_{OSS}			315		pF
Reverse Transfer Capacitance		C_{RSS}			90		pF
SWITCHING PARAMETERS							
Total Gate Charge		Q_G	V_{GS}=10V, V_{DS}=640V, I_D=12A (Note 1, 2)		123	155	nC
Gate to Source Charge		Q_{GS}			27	45	nC
Gate to Drain Charge		Q_{GD}			49	80	nC
Turn-ON Delay Time		$t_{D(ON)}$	V_{DD}=400V, I_D=12A, R_G=25Ω (Note 1, 2)		100	120	ns
Rise Time		t_R			198	220	ns
Turn-OFF Delay Time		$t_{D(OFF)}$			340	360	ns
Fall-Time		t_F			180	200	ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Maximum Body-Diode Continuous Current		I_S				12	A
Maximum Body-Diode Pulsed Current		I_{SM}				48	A
Drain-Source Diode Forward Voltage		V_{SD}	I_S=12A, V_{GS}=0V			1.4	V
Body Diode Reverse Recovery Time		t_{rr}	V_{GS}=0V, I_S=12A, dI_F/dt=100A/μs (Note 1)		1000		ns
Body Diode Reverse Recovery Charge		Q_{RR}			17.0		μC

Note: 1. Pulse Test: Pulse width ≤ 300μs, Duty cycle ≤ 2%
2. Essentially independent of operating temperature

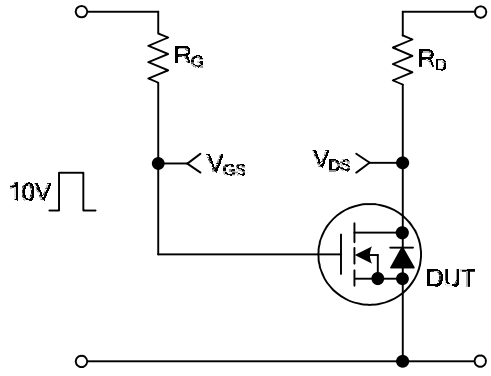
■ TEST CIRCUITS AND WAVEFORMS



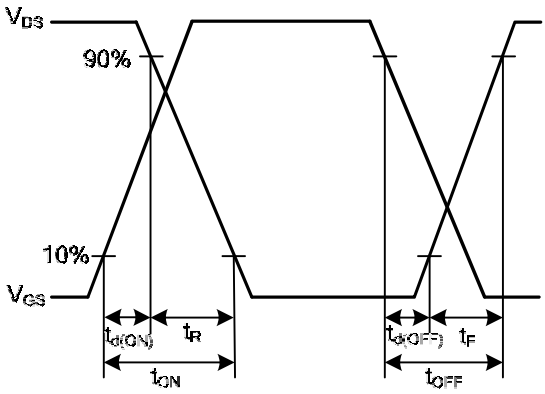
Gate Charge Test Circuit



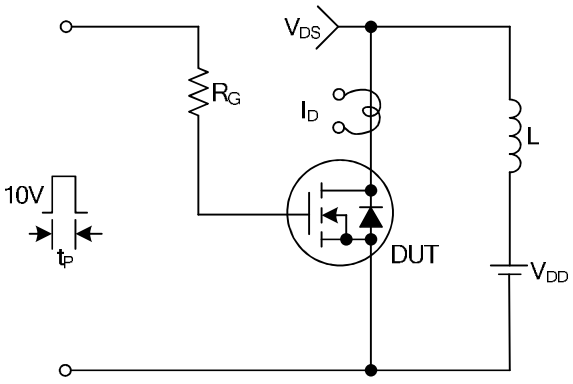
Gate Charge Waveforms



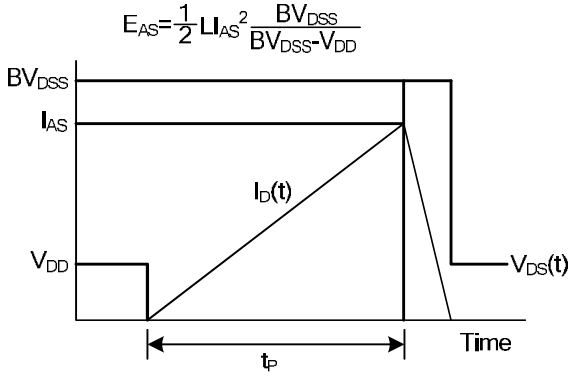
Resistive Switching Test Circuit



Resistive Switching Waveforms

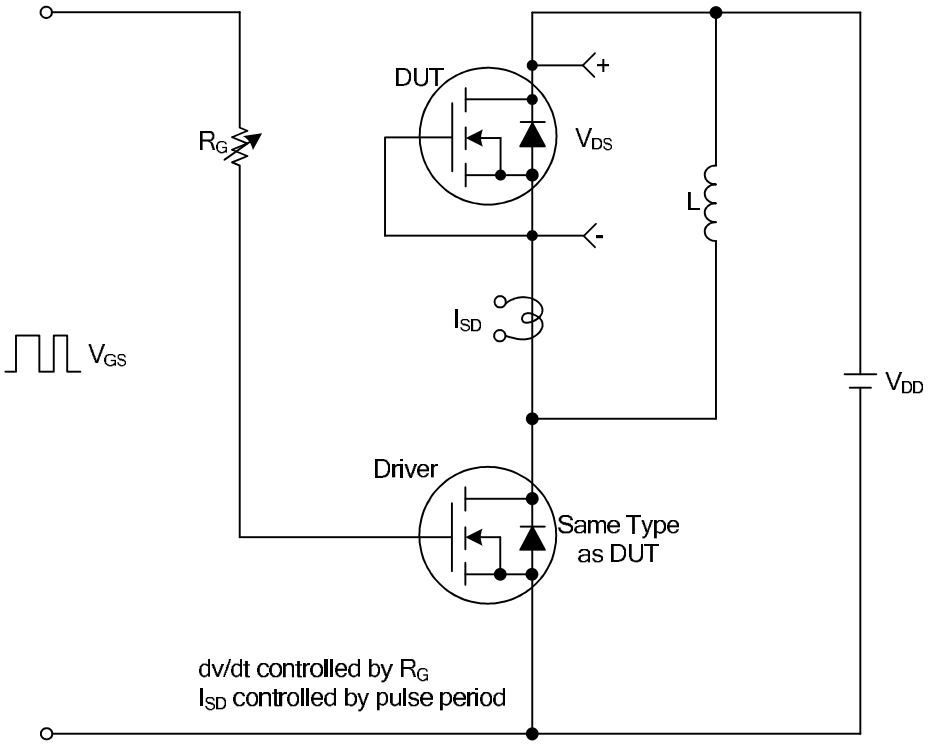


Unclamped Inductive Switching Test Circuit

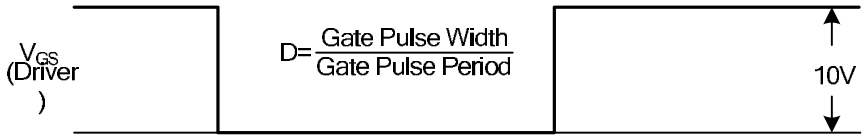


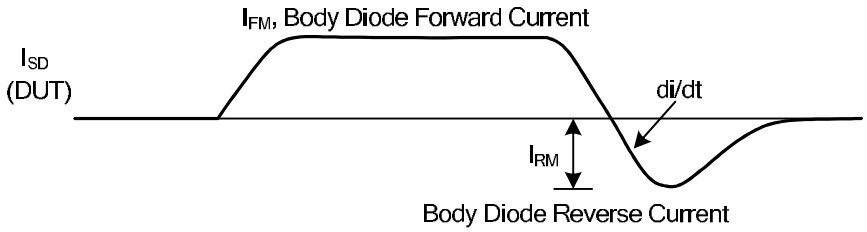
Unclamped Inductive Switching Waveforms

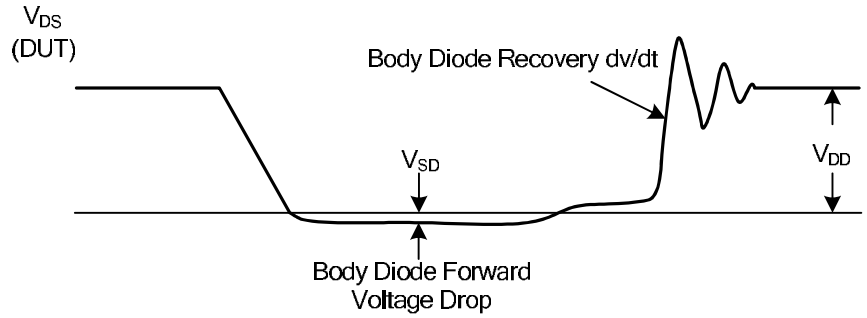
■ TEST CIRCUITS AND WAVEFORMS(Cont.)



Peak Diode Recovery dv/dt Test Circuit & Waveforms







■ TYPICAL CHARACTERISTICS