



DONGGUAN NANJING ELECTRONICS LTD.,

TO-220-3L Plastic-Encapsulate MOSFETS

NJ120N60 N-Channel MOSFET

V_{DSS}	$R_{DS(ON)}$ (Typ.)	I_D (Silicon limited current)
60V	4.5mΩ	120A

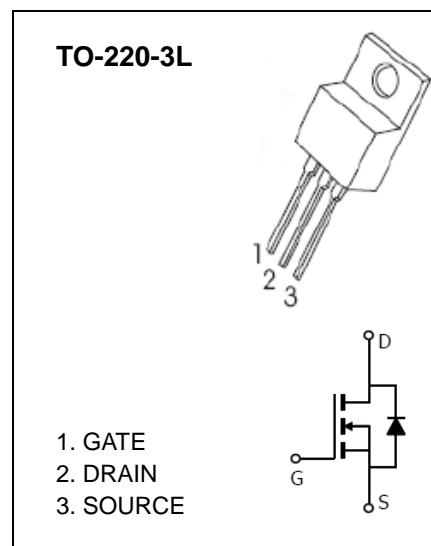
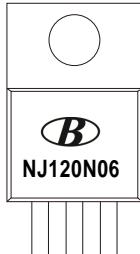
Applications:

- Adaptor
- Charger
- SMPS

Features:

- RoHS Compliant
- Low ON Resistance
- Low Gate Charge
- Peak Current vs Pulse Width Curve
- Inductive Switching Curves

MARKING



Absolute Maximum Ratings $T_C=25^\circ\text{C}$ unless otherwise specified

Symbol	Parameter	NJ120N06	Units
V_{DSS}	Drain-to-Source Voltage	60	V
I_D	Continuous Drain Current	120	A
	Continuous Drain Current $T_C = 100^\circ\text{C}$	76	A
I_{DM}	Pulsed Drain Current (NOTE *1)	480	A
V_{GS}	Gate-to-Source Voltage	± 20	V
E_{AS}	Single Pulse Avalanche Energy(NOTE *2)	822	mJ
T_L	Maximum Temperature for Soldering	300	
T_J and T_{STG}	Operating Junction and Storage Temperature Range	150, -55 to 150	$^\circ\text{C}$

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OFF Characteristics $T_c=25^\circ\text{C}$ unless otherwise specified

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
BV_{DSS}	Drain-to-Source Breakdown Voltage	60	--	--	V	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$
I_{DSS}	Drain-to-Source Leakage Current	--	--	1	μA	$\text{V}_{\text{DS}}=60\text{V}, \text{V}_{\text{GS}}=0\text{V}$ $T_a=25^\circ\text{C}$
		--	--	500		$\text{V}_{\text{DS}}=48\text{V}, \text{V}_{\text{GS}}=0\text{V}$ $T_a=125^\circ\text{C}$
I_{GSS}	Gate-to-Source Forward Leakage	--	--	+100	nA	$\text{V}_{\text{GS}}=+20\text{V}$
	Gate-to-Source Reverse Leakage	--	--	-100		$\text{V}_{\text{GS}}=-20\text{V}$

ON Characteristics $T_j=25^\circ\text{C}$ unless otherwise specified

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
$\text{R}_{\text{DS(ON)}}$	Static Drain-to-Source On-Resistance	--	4.5	6	$\text{m}\Omega$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=40\text{A}$
$\text{V}_{\text{GS(TH)}}$	Gate Threshold Voltage	2	3	4	V	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\mu\text{A}$
Pulse width $\leqslant 300\mu\text{s}$; duty cycle $\leqslant 2\%$						

Dynamic Characteristics Essentially independent of operating temperature

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
R_g	Gate Resistance	--	1.0	--	Ω	$f=1\text{MHz}, \text{V}_{\text{GS}}=0\text{V}, \text{V}_{\text{DS}}=0\text{V}$
C_{iss}	Input Capacitance	--	3238	--	pF	$\text{V}_{\text{GS}}=0\text{V}, \text{V}_{\text{DS}}=25\text{V}$ $f=1.0\text{MHz}$
C_{oss}	Output Capacitance	--	580	--		
C_{rss}	Reverse Transfer Capacitance	--	411	--	nC	$\text{I}_D=60\text{A}, \text{V}_{\text{DD}}=48\text{V}$ $\text{V}_{\text{GS}}=10\text{V}$
Q_g	Total Gate Charge	--	76.7	--		
Q_{gs}	Gate-to-Source Charge	--	14.1	--		
Q_{gd}	Gate-to-Drain ("Miller") Charge	--	34.3	--		

Resistive Switching Characteristics Essentially independent of operating temperature

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
$t_{\text{d(ON)}}$	Turn-on Delay Time	--	24.4	--	ns	$\text{V}_{\text{DD}}=30\text{V}, \text{I}_D=60\text{A}, \text{V}_{\text{G}}=10\text{V} \text{ R}_{\text{G}}=6\Omega$
t_{rise}	Rise Time	--	20	--		
$t_{\text{d(OFF)}}$	Turn-Off Delay Time	--	57.8	--		
t_{fall}	Fall Time	--	26	--		

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Source-Drain Diode Characteristics

T_c=25°C unless otherwise specified

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
I _S	Continuous Source Current (Body Diode)	--	--	120	A	T _c =25°C
I _{SM}	Maximum Pulsed Current (Body Diode)	--	--	480	A	
V _{SD}	Diode Forward Voltage	--	--	1.2	V	I _{SD} =30A, V _{GS} =0V
t _{rr}	Reverse Recovery Time	--	35.9	--	ns	I _F =20A di/dt=100A/us
Q _{rr}	Reverse Recovery Charge	--	40.4	--	nC	
Pulse width ≤300μs; duty cycle ≤ 2%						

Notes:

*1. Repetitive rating; pulse width limited by maximum junction temperature.

*2. L=0.5mH, I_D=57.3A, Start T_J=25°C

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Test Circuits and Waveforms

Figure 14. Gate Charge Test Circuit

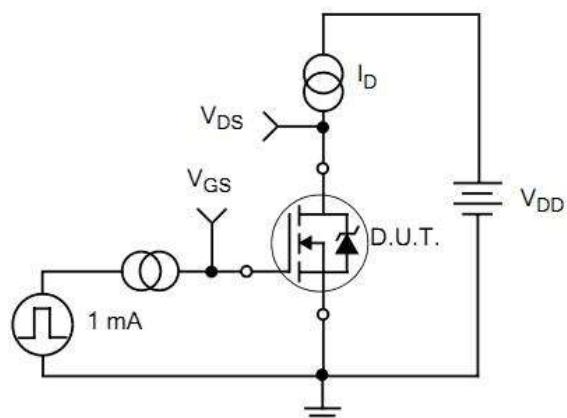


Figure 15. Gate Charge Waveforms

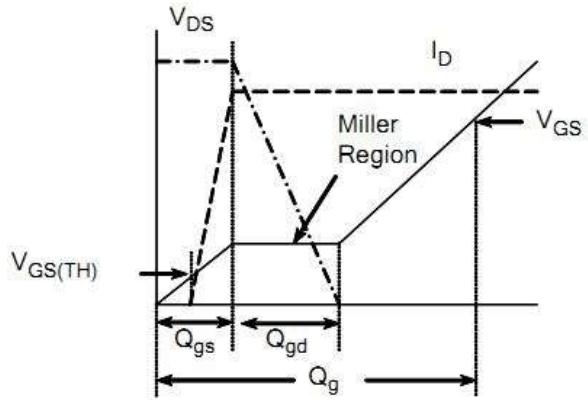


Figure 16. Resistive Switching Test Circuit

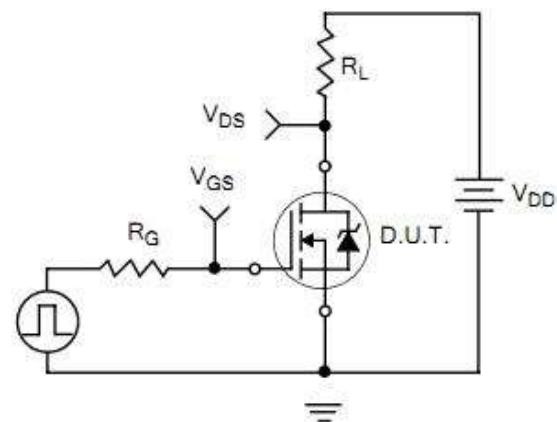
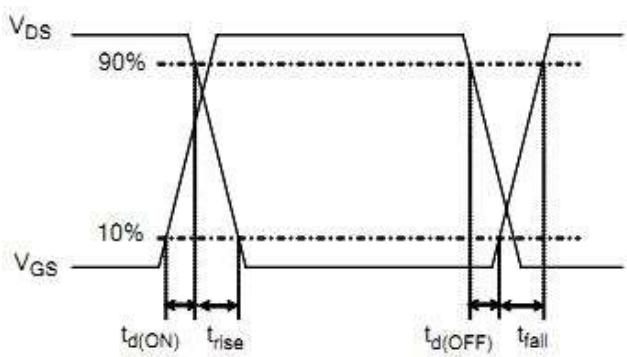


Figure 17. Resistive Switching Waveforms



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Figure 18. Diode Reverse Recovery Test Circuit

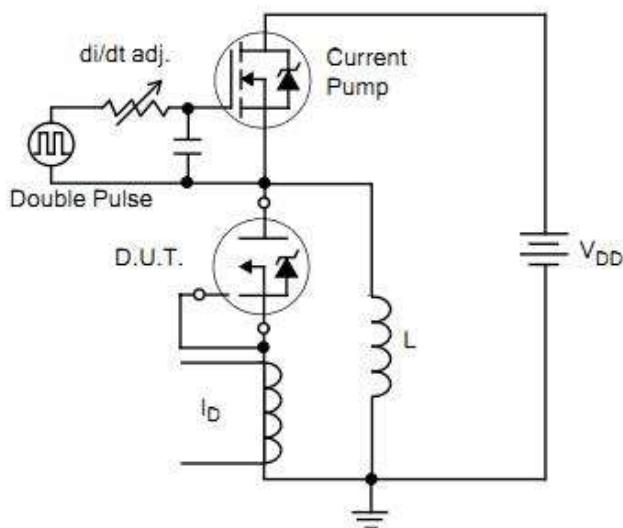


Figure 19. Diode Reverse Recovery Waveform

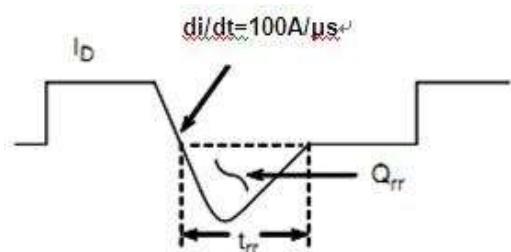


Figure 20. Unclamped Inductive Switching Test Circuit

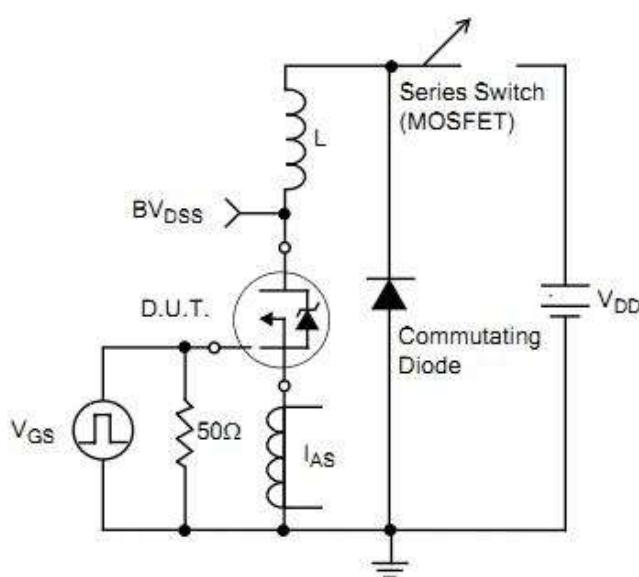
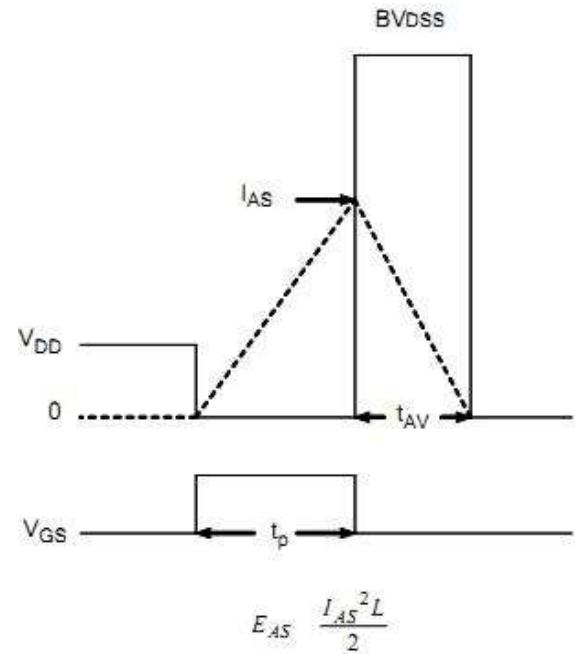
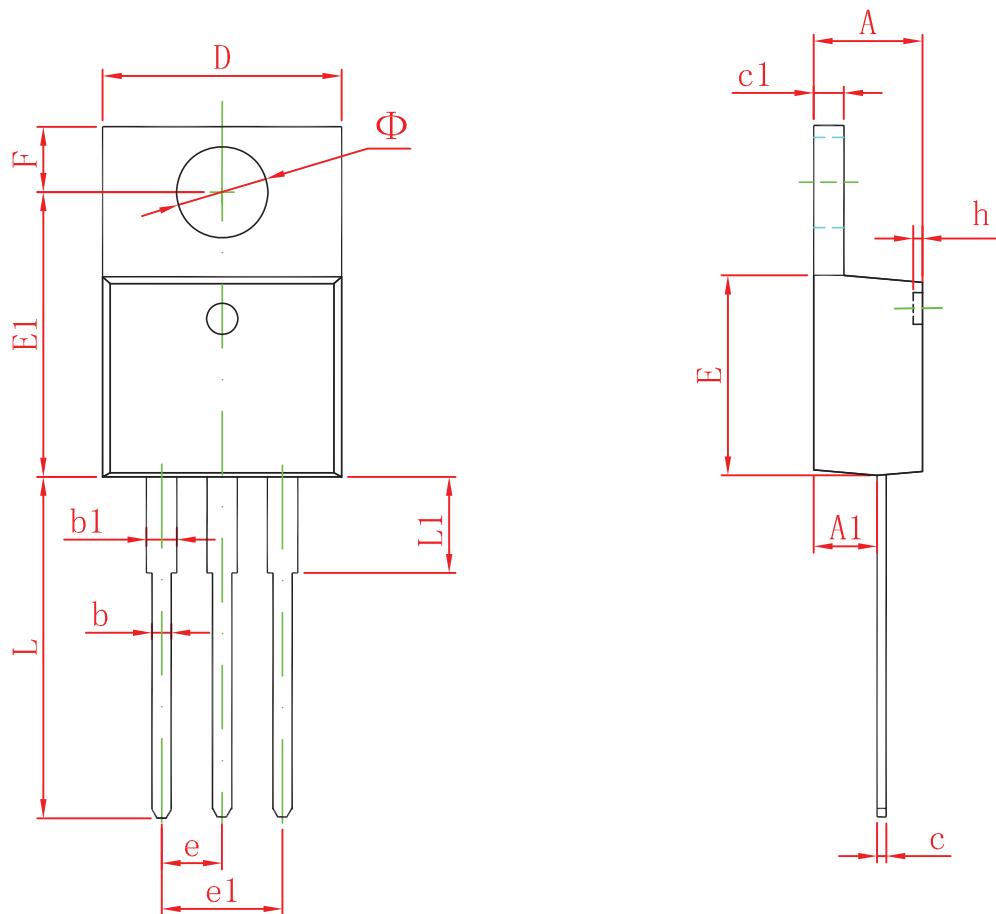


Figure 21. Unclamped Inductive Switching Waveform



TO-220 Package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	4.450	4.750	0.175	0.187
A1	2.520	2.820	0.099	0.111
b	0.710	0.910	0.028	0.036
b1	1.170	1.370	0.046	0.054
c	0.300	0.500	0.012	0.020
c1	1.170	1.370	0.046	0.054
D	9.830	10.330	0.387	0.407
E	8.500	8.900	0.335	0.350
E1	12.050	12.650	0.474	0.498
e	2.540 TYP		0.100 TYP	
e1	4.900	5.200	0.192	0.205
F	2.540	2.940	0.100	0.116
h	0.100 TYP		0.004 TYP	
L	13.300	13.800	0.523	0.543
L1	3.540	3.940	0.139	0.155
Φ	3.735	3.935	0.147	0.155