



DONGGUAN NANJING ELECTRONICS LTD.,

TO-247-3 Silicon Carbide Schottky Diode

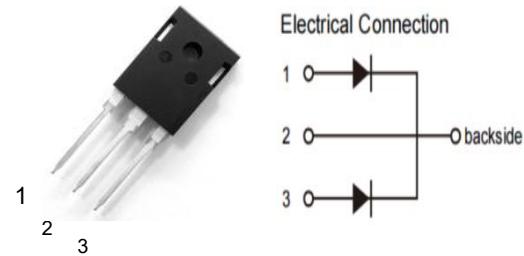
NJ65C30T3D SiC Diode 650 V, 30A, 112 nC

General Description

This product family offers state of the art performance. It is designed for high frequency applications where high efficiency and high reliability are required.

Features

- Zero Forward/Reverse Recovery Current
- High Blocking Voltage
- High Frequency Operation
- Positive Temperature Coefficient on VF
- Temperature Independent Switching Behavior



**TO-247-3
Pin definition**

Applications

- Motor Drives
- Solar
- AC/DC converters
- DC/DC converters
- Uninterruptable power supplies

Benefits

- Higher System Efficiency
- Parallel Device Convenience without thermal runaway
- Higher Temperature Application
- No Switching loss
- Hard Switching & Higher Reliability
- Environmental Protection

Key performance parameters

Type	V_R	I_F $T_C=150^\circ\text{C}$	Q_C
NJ65C30T3D	650 V	30A	112 nC

Caution: This device is sensitive to electrostatic discharge. Users should follow ESD handing procedures.

Typical Characteristics

Maximum Ratings

$T_c=25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V_{RRM}	650	V
Peak Reverse Surge Voltage	V_{RSM}	650	V
DC Blocking Voltage	V_R	650	V

Maximum Ratings

$T_c=25^\circ\text{C}$, unless otherwise specified

Symbol	Parameter	Test conditions	Value	Unit
V_{RRM}	Repetitive peak reverse voltage		650	V
I_F	Continuous forward current	$T_c=25^\circ\text{C}$ $T_c=155^\circ\text{C}$	60*/120** 15*/30**	A
I_{FSM}	Non-Repetitive forward surge current	$T_c=25^\circ\text{C}$, $t_p=10\text{ms}$, Half Sine Wave	120*	A
$\int i^2 dt$	$i^2 t$ value	$T_c=25^\circ\text{C}$, $t_p=10\text{ms}$	72	A^2s
P_{tot}	Power dissipation	$T_c=25^\circ\text{C}$ $T_c=110^\circ\text{C}$		W
T_j	Operating junction temperature		-55~175	$^\circ\text{C}$
T_{stg}	Storage temperature		-55~175	$^\circ\text{C}$

Thermal Resistance

Parameter	Symbol	Typ.	Max	Unit
Thermal resistance, junction-case	R_{thJC}	0.69* 0.35**		$^\circ\text{C}/\text{W}$

* Per leg

** Per Device

Typical Characteristics

Electrical Characteristic

$T_C = 25^\circ\text{C}$, unless otherwise specified

Symbol	Parameter	Test conditions	Value			Unit
			Min.	Typ.	Max.	
V_{DC}	DC blocking voltage	$T_j=25^\circ\text{C}$	650			V
V_F	Diode forward voltage	$I_F=15\text{A}, T_j=25^\circ\text{C}$ $I_F=15\text{A}, T_j=135^\circ\text{C}$ $I_F=15\text{A}, T_j=175^\circ\text{C}$		1.28 1.45 1.58	1.55 1.75 2.2	V
I_R	Reverse current	$V_R=650\text{V}, T_j=25^\circ\text{C}$ $V_R=650\text{V}, T_j=175^\circ\text{C}$		1 18	50 200	μA

AC Characteristic

Symbol	Parameter	Test conditions	Value			Unit
			Min.	Typ.	Max.	
Q_C	Total capacitive charge	$V_R=400\text{V}, T_j=25^\circ\text{C}$ $Q_C = \int_0^V R_C(V)dV$		112**		nC
C	Total capacitance	$V_R=1\text{V} f=1\text{MHz}$ $V_R=300\text{V} f=1\text{MHz}$ $V_R=600\text{V} f=1\text{MHz}$		826 91 79		pF
E_C	Capacitance stored energy	$V_R=400\text{V}$		8.5		μJ

Typical Characteristics

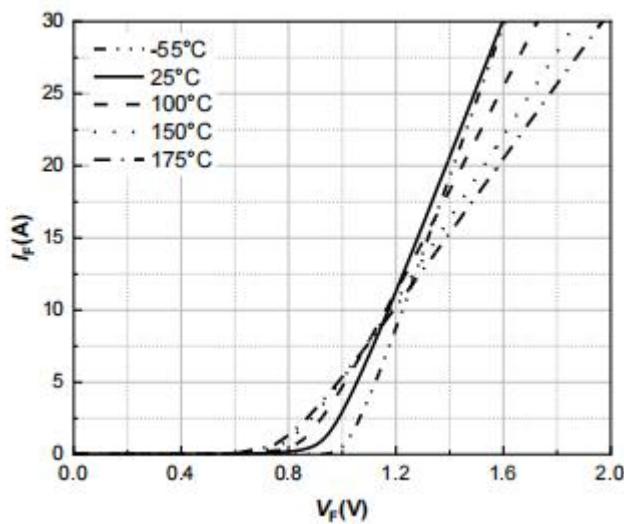


Figure 1. Typical forward characteristics

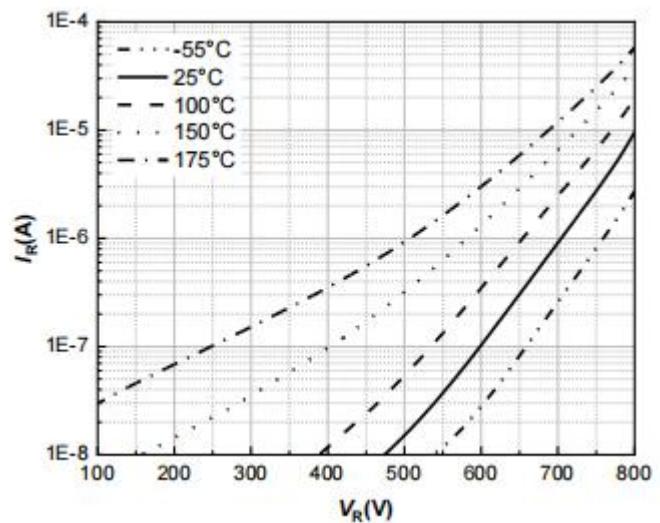


Figure 2. Typical reverse current as function of reverse voltage

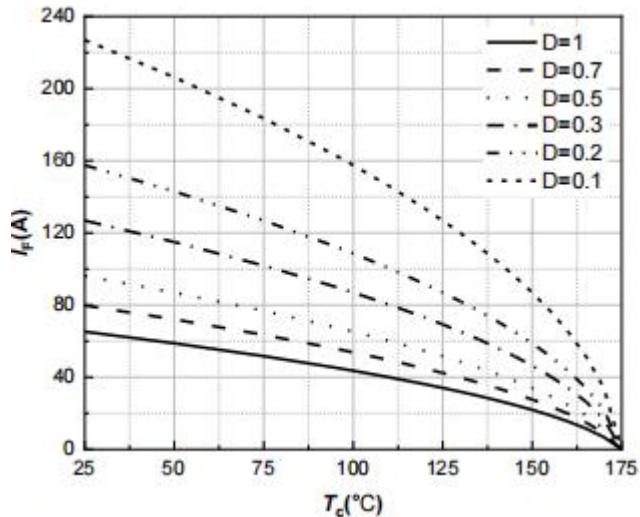


Figure 3. Diode forward current as function of temperature, D=duty cycle

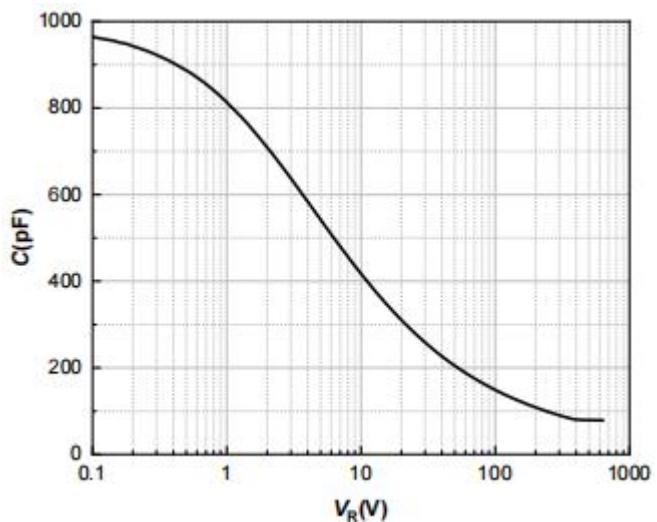


Figure 4. Typical capacitance as function of reverse voltage, $C=f(V_R)$; $T_j=25^\circ\text{C}$; $f=1 \text{ MHz}$

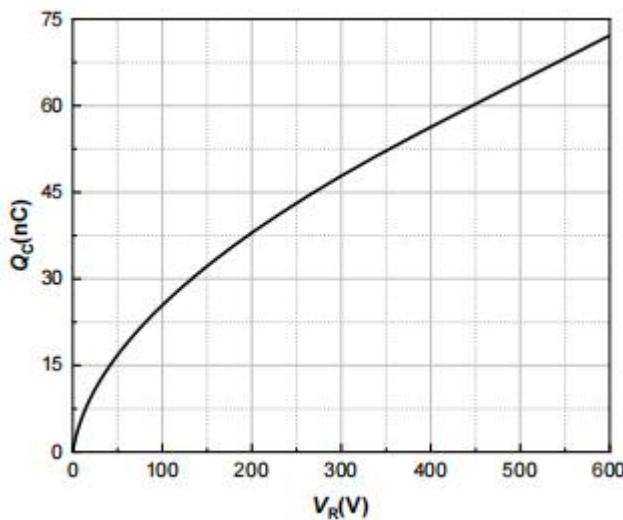


Figure 5. Typical reverse charge as function of reverse voltage

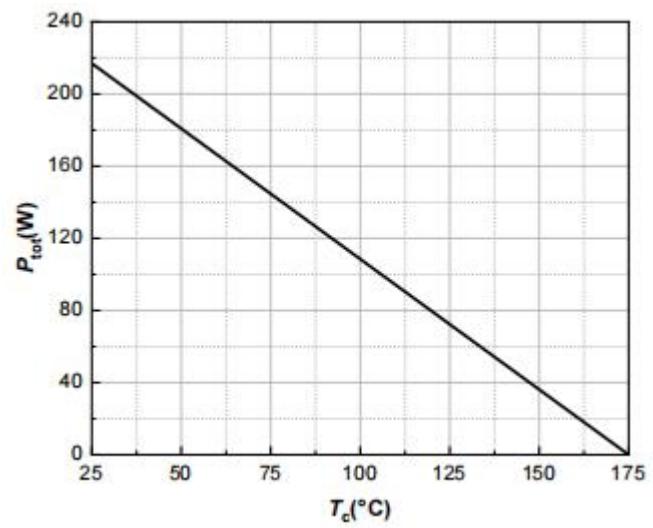


Figure 6. Power dissipation as function of case temperature

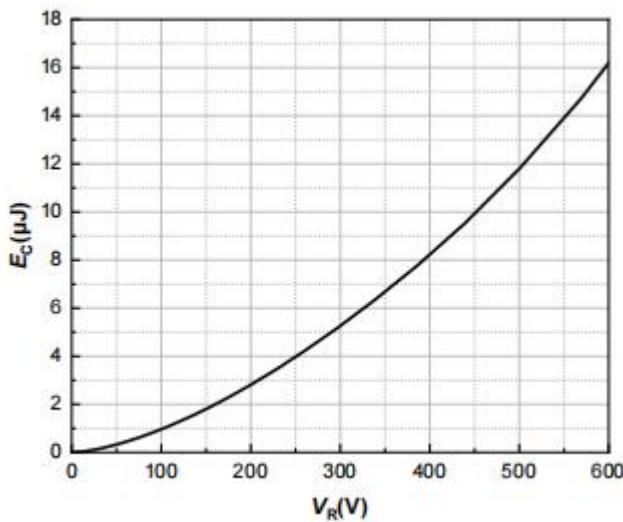


Figure 7. Capacitance stored energy

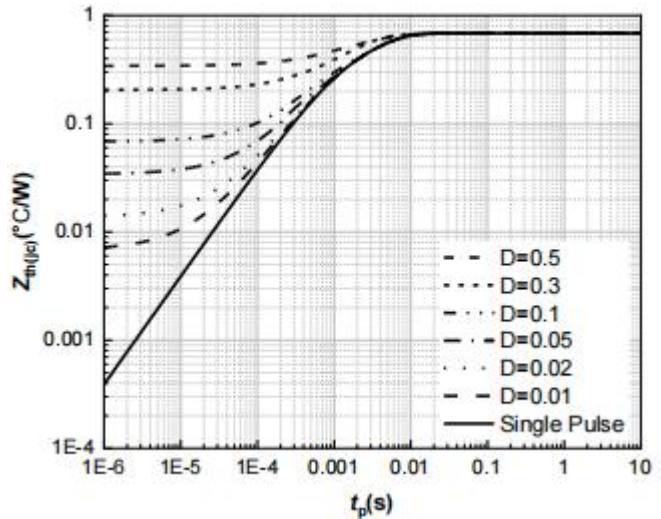
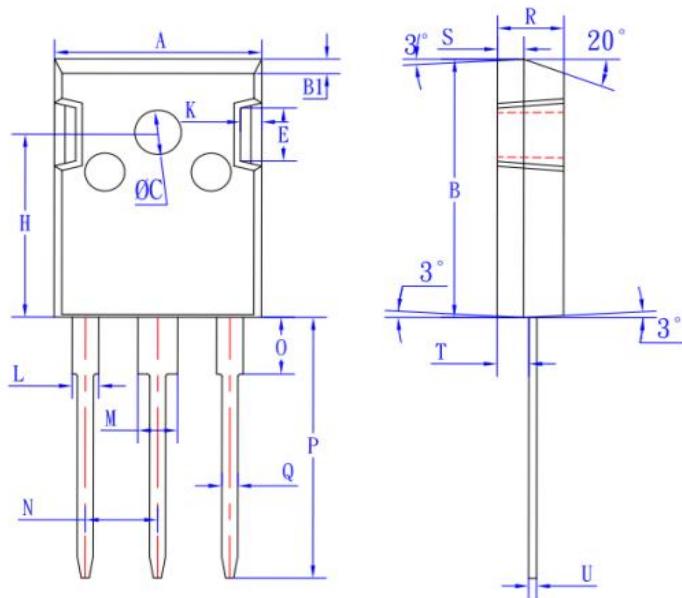


Figure 8. Max.transient thermal impedance, $Z_{th(jc)}=f(t_p)$,parameter: $D=t_p/T$

Package Outline Dimensions

Package Outline: TO-247-3



Dim.	Min.	Max.
A	15.51	15.71
B	20.42	20.52
B1	0.89	1.12
C	3.62	4.59
E	4.50	5.05
H	14.22	14.56
K	1.50	1.68
L	2.05	2.15
M	2.95	3.10
N	5.41	5.46
O	4.25	4.40
P	20.51	20.76
Q	1.17	1.23
R	4.95	5.05
S	1.98	2.04
T	2.35	2.41
U	0.60	0.66
All Dimensions in millimeter		